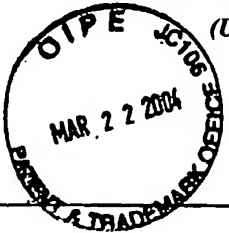

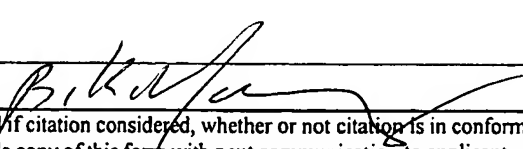


Date: March 19, 2004

Page 1 of 1

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				ATTY. DOCKET NO. TI-37372 (UNITI-181XX)		APPLICATION NO. 10/724,296	
				APPLICANT: Nitin Agarwal et al			
				FILING DATE November 28, 2003		TC ART UNIT UnAssigned	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION/ ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE	
	US						
	US						
	US						
	US						
	US						
	US						
	US						
	US						
FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO	
OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)							
	Rabaey, J. M., "Digital Integrated Circuits: A Design Perspective" Published by Prentice-Hall, Inc., Second Edition, September 2000.						
EXAMINER				DATE CONSIDERED 8/16/04			
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

JFT/raw 303658-1